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 IBM Technical Disclosure Bulletins

Term:

((combining or combine) adj5 address) same thread

Display: Documents in Display Format: Starting with Number

Generate: Hit List Hit Count Side by Side Image

Search History

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side by side		
DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ		
<u>L30</u> ((combining or combine) adj5 address) same thread	4	<u>L30</u>
<u>L29</u> L28 and thread	1	<u>L29</u>
<u>L28</u> (combine or combining) adj5 mask adj5 address	10	<u>L28</u>
<u>L27</u> L26 same address	4	<u>L27</u>
<u>L26</u> static adj2 thread	98	<u>L26</u>
<u>L25</u> (associate or associating) same mask same thread	14	<u>L25</u>
<u>L24</u> l23 and address	12	<u>L24</u>
<u>L23</u> static thread	36	<u>L23</u>
<u>L22</u> (thread) same address and static	1179	<u>L22</u>
<u>L21</u> (thread adj4 mask) same address and static	6	<u>L21</u>
<u>L20</u> (thread adj4 mask) same address andf static	0	<u>L20</u>
<u>L19</u> (thread adj4 mask) same address	10	<u>L19</u>
<u>L18</u> thread adj4 mask	137	<u>L18</u>
<u>L17</u> thread same mask	1925	<u>L17</u>
<u>L16</u> thread adj2 static	159	<u>L16</u>

<u>L15</u>	thread adj2 static adj2 data	8	<u>L15</u>
<u>L14</u>	L12 and mask and base address	7	<u>L14</u>
<u>L13</u>	L12 and mask	20	<u>L13</u>
<u>L12</u>	L11 and l2	155	<u>L12</u>
<u>L11</u>	l8 and thread	280	<u>L11</u>
<u>L10</u>	l9	0	<u>L10</u>
<i>DB=EPAB; PLUR=YES; OP=ADJ</i>			
<u>L9</u>	thread same l8	0	<u>L9</u>
<i>DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>			
<u>L8</u>	static adj2 variable	1345	<u>L8</u>
<u>L7</u>	address mask same thread	5	<u>L7</u>
<u>L6</u>	l5 and l4	3	<u>L6</u>
<u>L5</u>	(combine or combining) same ((address adj2 mask) or mask) same address	352	<u>L5</u>
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<u>L3</u>	address same mask same thread	75	<u>L3</u>
<u>L2</u>	(multi or multiple) adj2 (thread or threaded)	10995	<u>L2</u>
<u>L1</u>	thread	467007	<u>L1</u>

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